

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the application:

**Listing of Claims:**

1 (currently amended): A graphics card for smoothing the playing of video

5 comprising:

a video capture engine for receiving a digital video signal;

a memory connected to the video capture engine for storing the  
digital signal captured by the video capture engine without  
~~special processing~~ downscaling the digital video signal;

10 a video process engine connected to the memory for processing the digital video  
signal of the memory into digital video data and then storing the digital  
video data in the memory;

a video display engine connected to the memory for receiving the digital signal  
or the digital video data stored in the memory; and

15 a first clock generator connected to the video capture engine and the video  
display engine for providing the same clock having the same frequency and  
the same phase to the video capture engine and the video display engine so  
as to synchronize the signal receiving rates of the video capture engine and  
the video display engine.

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2 (original): The graphics card of claim 1 further comprising a decoder  
connected to the video capture engine for transforming a received  
analog video signal into the digital video signal and outputting the  
digital video signal to the video capture engine.

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3 (original): The graphics card of claim 2 wherein the first clock generator is located in  
the decoder.

4 (original): The graphics card of claim 1 further comprising an encoder connected to the video display engine.

5 5 (previously presented): The graphics card of claim 4 wherein the first clock generator is further connected to the encoder for providing the same clock having the same frequency and the same phase to the video capture engine, the video display engine, and the encoder.

10 6 (cancelled).

7 (original): The graphics card of claim 1 further comprising an output interface connected to the video display engine for outputting the digital video signal from the video display engine.

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8 (previously presented): The graphics card of claim 7 wherein the first clock generator is further connected to the output interface for providing the same clock having the same frequency and the same phase to the video capture engine, the video display engine, and the output interface.

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9 (original): The graphics card of claim 7 wherein the output interface is a liquid crystal display (LCD) output interface, a cathode ray tube (CRT) output interface, or a plasma display panel (PDP) output interface.

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10 (cancelled).

11 (previously presented): The graphics card of claim 1 further comprising

a video process clock generator connected to the video process engine for providing a clock to the video process engine.

12 (currently amended): A graphics card for smoothing the playing of

5 video comprising:

a video capture engine for receiving a digital video signal;

a memory connected to the video capture engine for storing the digital signal captured by the video capture engine without ~~special processing~~ downscaling the digital video signal;

10 a video process engine connected to the memory for processing the digital video signal of the memory into digital video data and then storing the digital video data in the memory;

a video display engine connected to the memory for receiving the digital signal of the digital video data stored in the memory;

15 a multiplexer having an output connected to the video display engine;

a first clock generator connected to the video capture engine and a first input of the multiplexer for providing a first clock to the video capture engine and the first input of the multiplexer, the first clock provided to the video capture engine having the same frequency and the same phase as the first clock provided to the first input of the multiplexer; and

20 a second clock generator connected to the first clock generator and a second input of the multiplexer for receiving the first clock to generate a second clock, the second clock having substantially the same frequency as the first clock and being offset from the first clock by a predetermined phase difference.

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13 (original): The graphics card of claim 12 further comprising a decoder connected to the video capture engine for transforming a received

analog video signal into the digital video signal and outputting the digital video signal to the video capture engine.

14 (original): The graphics card of claim 13 wherein the first clock generator is located in  
5 the decoder.

15 (original): The graphics card of claim 12 further comprising an encoder connected to the video display engine.

10 16 (previously presented): The graphics card of claim 15 wherein the output of the multiplexer is further connected to the encoder for providing the same clock having the same frequency and the same phase to the video display engine and the encoder.

15 17 (cancelled).

18 (original): The graphics card of claim 12 further comprising an output interface connected to the video display engine for outputting the digital video signal from the video display engine.

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19 (previously presented): The graphics card of claim 18 wherein the output of the multiplexer is further connected to the output interface for providing the same clock having the same frequency and the same phase to the video display engine and the output interface.

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20 (original): The graphics card of claim 18 wherein the output interface is a liquid crystal display (LCD) output interface, a cathode ray tube (CRT) output interface, or a plasma display panel (PDP) output

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interface.

21 (cancelled).

- 5    22 (previously presented): The graphics card of claim 12 further comprising a video process clock generator connected to the video process engine for providing a clock to the video process engine.